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APPLICANTS Yoshihiro Okada, Hashima-Shi, JAPAN; Takahiko Ogo, Hashima-Shi, JAPAN; Kaoru Sasaki, Iwami-Gun, Tottori, JAPAN;				
** CONTINUING DATA ***** This application is a 371 of PCT/JP03/14516 11/14/2003				
** FOREIGN APPLICATIONS ***** JAPAN 2002-334585 11/19/2002				
Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Allowance Verified and Acknowledged _____ Examiner's Signature Initials		STATE OR COUNTRY JAPAN	SHEETS DRAWING 11	TOTAL CLAIMS 9
INDEPENDENT CLAIMS 3				
ADDRESS 26021				
TITLE Semiconductor integrate device and method for manufacturing same				
FILING FEE RECEIVED 900	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	